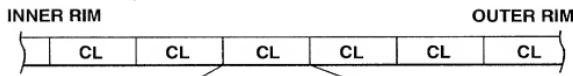
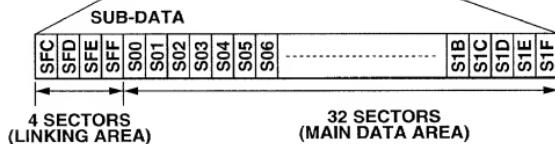


FIG.1

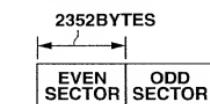
**FIG.2A**



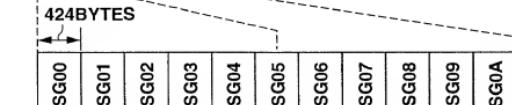
**FIG.2B**



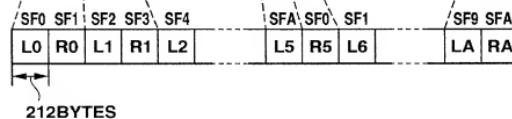
**FIG.2C**



**FIG.2D**



**FIG.2E**



HEADER		16bit		16bit			
MSB	LSB	MSB	LSB	MSB	LSB	MSB	LSB
00000000	11111111	11111111	11111111	11111111	00000000	0	0
11111111	11111111	11111111	11111111	11111111	11111111	1	1
11111111	11111111	11111111	11111111	00000000	00000000	2	2
Cluster · H	Cluster1	Sector	00000010	00000000	00000000	3	3
00000000	00000000	00000000	00000000	00000000	00000000	4	4
00000000	00000000	00000000	00000000	00000000	00000000	5	5
00000000	00000000	00000000	00000000	00000000	00000000	6	6
Marker code	Model code	First TNO	Last TNO	00000000	00000000	7	7
00000000	00000000	00000000	Used Sectors	00000000	00000000	8	8
00000000	00000000	00000000	00000000	00000000	00000000	9	9
00000000	00000000	00000000	Disc Serial No	00000000	00000000	10	10
Disk	ID	P-DFA	P-EMPTY	00000000	00000000	11	11
P-FRA	P-TNO1	P-TNO2	P-TNO3	00000000	00000000	12	12
P-TNO4	P-TNO6	P-TNO6	P-TNO7	00000000	00000000	13	13
ACCOMMODATING TABLE INDICATING DATA WIDTH							
(01h)	P-TNO248	P-TNO249	P-TNO250	P-TNO251	74		
	P-TNO252	P-TNO253	P-TNO254	P-TNO255	75		
	00000000	00000000	00000000	00000000	76		
	00000000	00000000	00000000	00000000	77		
(02h)	START ADDRESS	END ADDRESS	END ADDRESS	TRACK MODE	78		
	START ADDRESS	END ADDRESS	END ADDRESS	LINK INFORMATION	79		
(03h)	START ADDRESS	END ADDRESS	END ADDRESS	TRACK MODE	80		
	START ADDRESS	END ADDRESS	END ADDRESS	LINK INFORMATION	81		
	END ADDRESS	END ADDRESS	END ADDRESS	TRACK MODE	82		
	END ADDRESS	END ADDRESS	END ADDRESS	LINK INFORMATION	83		
MANAGEMENT TABLE WIDTH (256 SLOTS)							
(FCh)	START ADDRESS	END ADDRESS	END ADDRESS	TRACK MODE	580		
	START ADDRESS	END ADDRESS	END ADDRESS	LINK INFORMATION	581		
(FDh)	START ADDRESS	END ADDRESS	END ADDRESS	TRACK MODE	582		
	START ADDRESS	END ADDRESS	END ADDRESS	LINK INFORMATION	583		
(FEh)	START ADDRESS	END ADDRESS	END ADDRESS	TRACK MODE	584		
	START ADDRESS	END ADDRESS	END ADDRESS	LINK INFORMATION	585		
(FFh)	START ADDRESS	END ADDRESS	END ADDRESS	TRACK MODE	586		
	END ADDRESS	END ADDRESS	END ADDRESS	LINK INFORMATION	587		

U-TOC SECTOR 0

FIG.3

**FIG.4A**

P - FRA = 03h

**FIG.4B**

	START ADDRESS	END ADDRESS	LINK INFORMATION
(03h)	S03	E03	18h

**FIG.4C**

(18h)	S18	E18	1Fh
-------	-----	-----	-----

**FIG.4D**

(1Fh)	S1F	E1F	2Bh
-------	-----	-----	-----

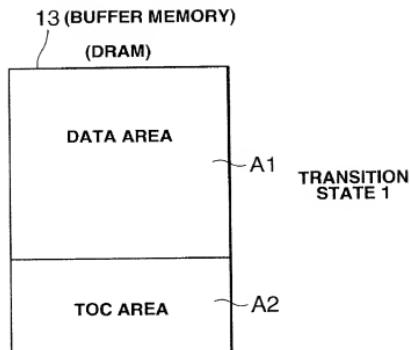
**FIG.4E**

(2Bn)	S2B	E2B	E3H
-------	-----	-----	-----

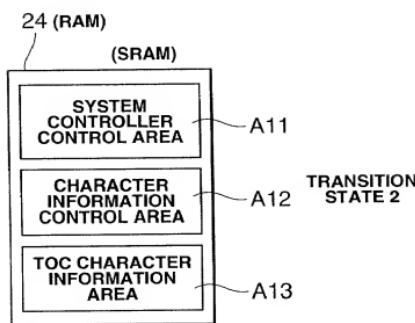
**FIG.4F**

(E3h)	SE3	EE3	00h
-------	-----	-----	-----

HEADER				16bit				16bit			
MSB	LSB	MSB	LSB	MSB	LSB	MSB	LSB	MSB	LSB	MSB	LSB
00000000	11111111	11111111	11111111	11111111	11111111	11111111	11111111	0			
11111111	11111111	11111111	11111111	11111111	11111111	00000000	00000000	1			
11111111	Cluster · H	Cluster2	Sector	00000010	00000010	00000000	00000000	2			
00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	3			
00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	4			
00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	5			
00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	6			
00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	7			
00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	8			
00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	9			
00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	1			
00000000	00000000	00000000	00000000	00000000	00000000	00000000	00000000	1			
00000000	P-TNA1	P-TNO2	P-TNA3	P-EMPTY	P-EMPTY	P-EMPTY	P-EMPTY	1			
P-TNA4	P-TNA5	P-TNO6	P-TNA7	P-EMPTY	P-EMPTY	P-EMPTY	P-EMPTY	1			
ACCOMMODATING TABLE INDICATING DATA WIDTH											



**FIG.6A**



**FIG.6B**

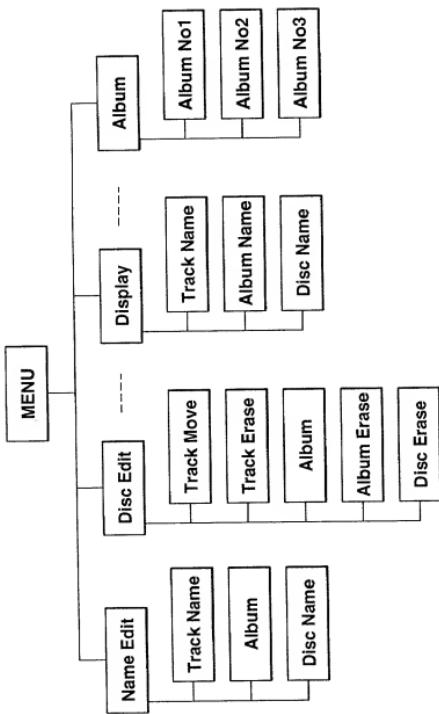


FIG.7

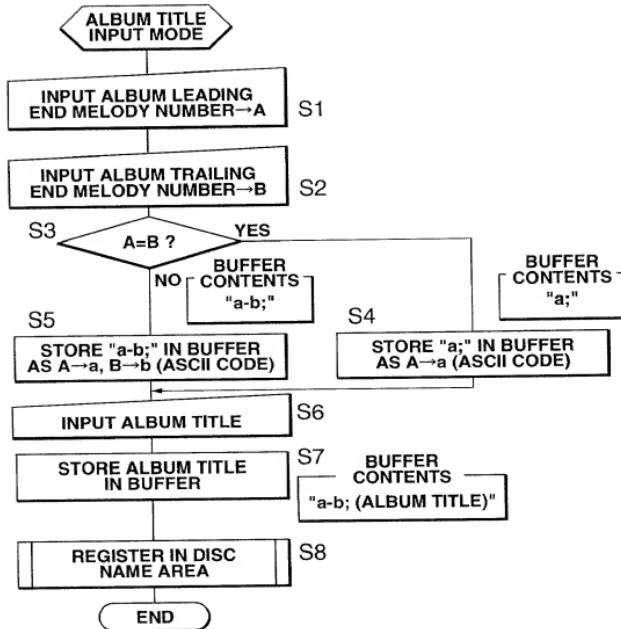


FIG.8

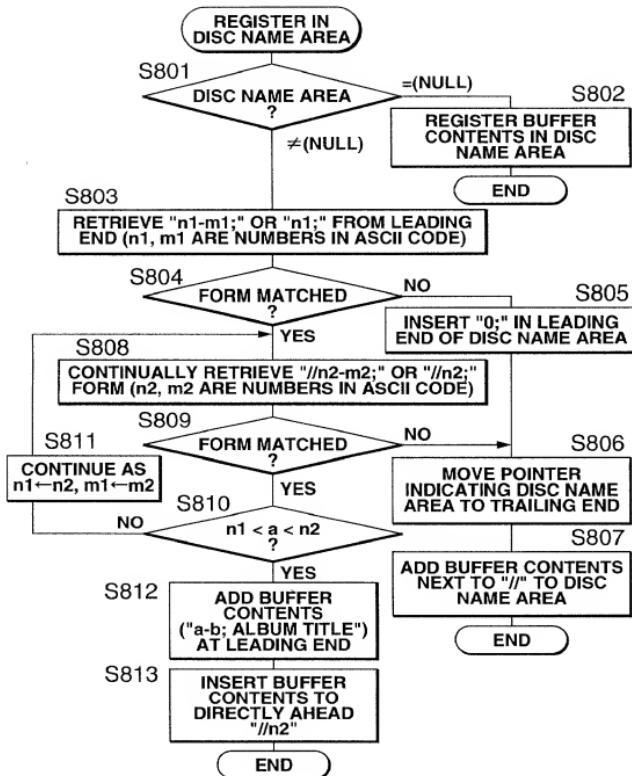


FIG.9

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
76	8	—	1	0
77	:	G	A	00
78	00	00	00	00
79	00	00	00	00
80	00	00	00	00
81	00	00	00	00
...	...	...	...	...
...	...	...	...	...

**FIG.10**

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
76	M	i	n	l
77	D	—	s	01
78	c	00	00	00
79	00	00	00	00
80	00	00	00	00
81	00	00	00	00
...	...	...	...	...

Byte position of the next slot  
 $=76 \times 4 + (\text{Link-P}) \times 8$

**FIG.11A**



12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
76	0	—	M	i
77	n	—	D	01
78	—	s	—	—
79	/	1	—	—
80	7	—	S	02
81	N	y	00	00
...	...	...	...	...

Byte position of the next slot  
 $=76 \times 4 + (\text{Link-P}) \times 8$

**FIG.11B**

12	0000000	P-TNA 1	P-TNA 2	P-TNA 3	
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7	
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11	
....	....	....	....	....	
....	....	....	....	....	
76	1	—	7	;	
77	S	O	N	01	Byte position of the next slot =76 × 4+(Link-P) × 8
78	Y	/	/	8	
79	—	1	0	02	
80	:	G	A	00	
81	00	00	00	00	
....	....	....	....	....	
....	....	....	....	....	

FIG.12

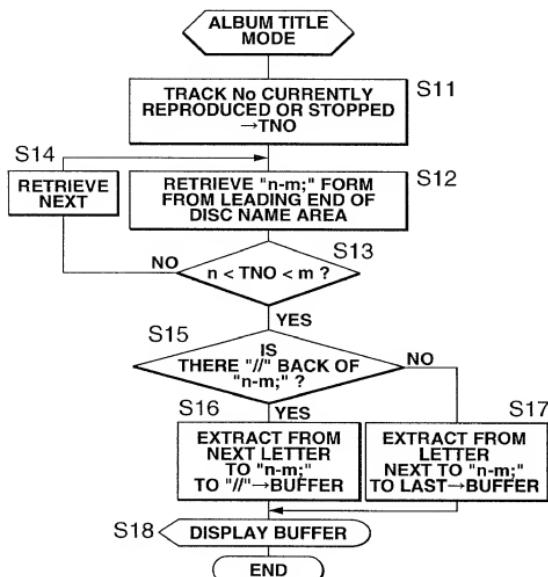
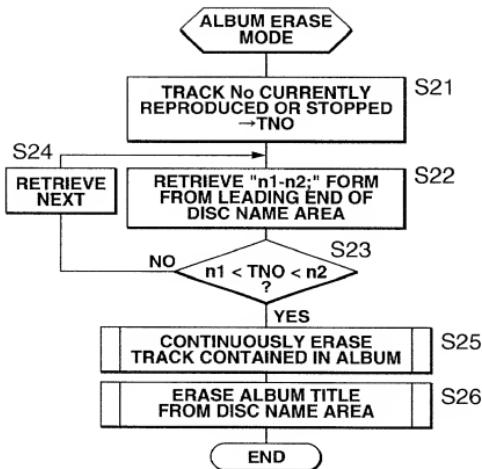


FIG.13

**FIG.14**

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
...	...	...	...	...
76	1	—	7	;
77	S	O	N	01
78	Y	/	/	8
79	—	1	0	02
80	;	G	A	/
81	/	1	1	03
82	—	2	0	;
83	M	—	n	04
84	I	D	—	s
85	c	00	00	00
...	...	...	...	...
...	...	...	...	...

Byte position of the next slot  
 $=76 \times 4 + (\text{Link-P}) \times 8$

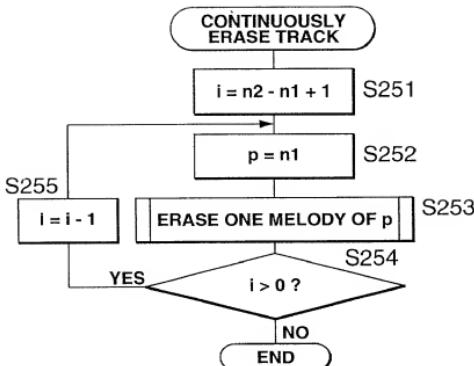


12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
...	...	...	...	...
76	1	—	7	;
77	S	O	N	01
78	Y	/	/	8
79	—	1	7	02
80	;	M	—	n
81	I	D	—	03
82	s	c	00	00
83	00	00	00	00
...	...	...	...	...
...	...	...	...	...

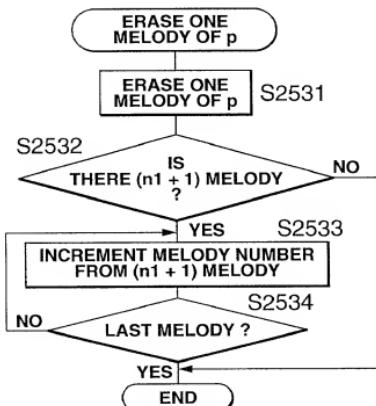
Byte position of the next slot  
 $=76 \times 4 + (\text{Link-P}) \times 8$

**FIG.15A**

**FIG.15B**



**FIG.16**



**FIG.17**

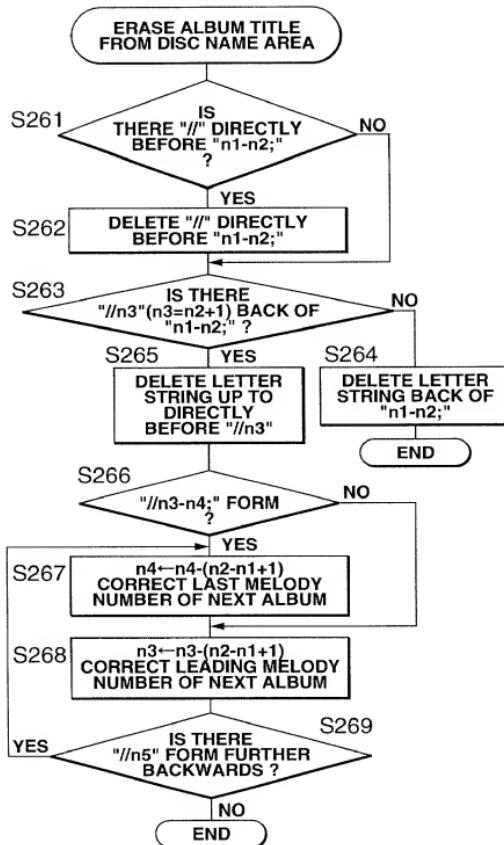
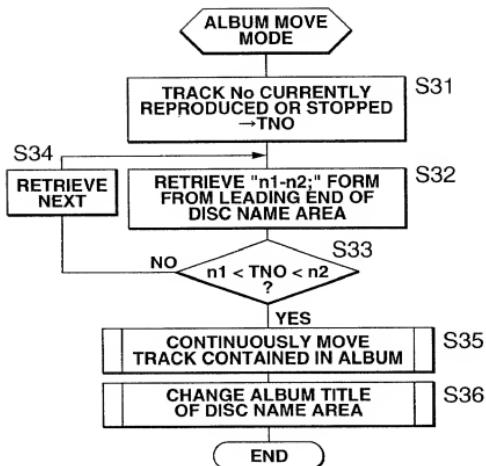


FIG.18



**FIG.19**

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
...	...	...	...	...
76	1	—	7	—
77	S	O	N	01
78	Y	/	/	8
79	—	1	0	02
80	—	G	A	/
81	—	1	1	03
82	—	2	0	—
83	M	—	n	04
84	—	D	—	s
85	c	00	00	00
...	...	...	...	...
...	...	...	...	...

Byte position of the next slot  
 $=76 \times 4 + (\text{Link-P}) \times 8$

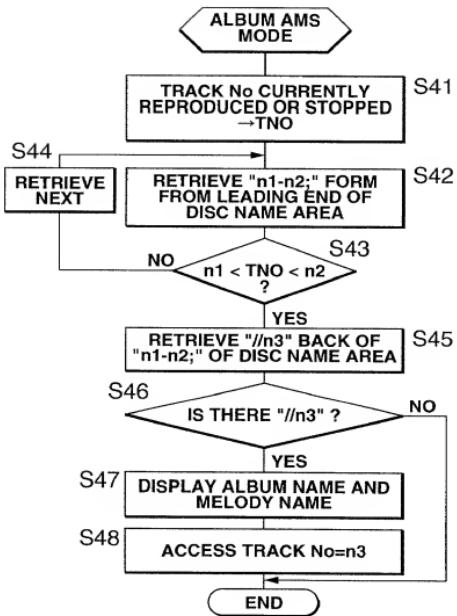


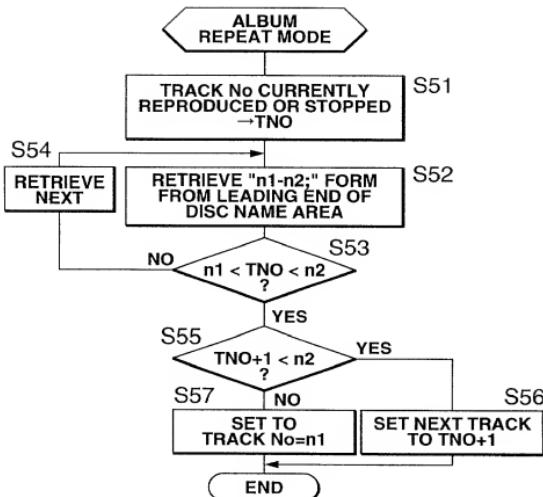
FIG.20A

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
...	...	...	...	...
76	1	—	7	—
77	S	O	N	01
78	Y	/	/	8
79	—	1	7	02
80	—	M	—	n
81	—	D	—	03
82	s	c	/	/
83	—	8	—	04
84	—	2	0	—
85	A	00	00	00
...	...	...	...	...
...	...	...	...	...

Byte position of the next slot  
 $=76 \times 4 + (\text{Link-P}) \times 8$

FIG.20B

**FIG.21**



**FIG.22**

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
...	...	...	...	...
76	1	—	7	—
77	S	O	N	01
78	Y	/	/	8
79	—	1	0	02
80	;	G	A	/
81	/	1	1	03
82	—	2	0	—
83	M	—	n	04
84	i	D	—	s
85	c	00	00	00
...	...	...	...	...
...	...	...	...	...

Byte position of the next slot  
 $=76 \times 4 + (\text{Link-P}) \times 8$



12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
...	...	...	...	...
76	1	—	1	0
77	;	S	O	01
78	N	Y	/	/
79	1	1	—	02
80	2	0	—	M
81	1	n	—	03
82	D	1	s	c
83	00	00	00	00
...	...	...	...	...
...	...	...	...	...

Byte position of the next slot  
 $=76 \times 4 + (\text{Link-P}) \times 8$

**FIG.23A**

**FIG.23B**

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
...	...	...	...	...
76	1	—	1	0
77	—	S	0	01
78	N	Y	/	/
79	1	1	—	02
80	2	0	—	M
81	—	n	—	03
82	D	—	s	c
83	00	00	00	00
...	...	...	...	...
...	...	...	...	...

Byte position of the next slot  
 $=76 \times 4 + (\text{Link-P}) \times 8$



12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
...	...	...	...	...
76	1	—	5	—
77	S	O	N	01
78	Y	/	/	6
79	—	1	0	02
80	—	/	/	1
81	1	—	2	03
82	0	—	M	—
83	n	—	D	04
84	—	s	c	00
85	00	00	00	00
...	...	...	...	...
...	...	...	...	...

Byte position of the next slot  
 $=76 \times 4 + (\text{Link-P}) \times 8$

**FIG.24A**

**FIG.24B**

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
...	...	...	...	...
76	1	—	7	—
77	S	0	N	01
78	Y	/	/	8
79	—	1	0	02
80	j	G	A	/
81	j	1	1	03
82	—	2	0	—
83	M	—	n	04
84	I	D	—	s
85	c	00	00	00
...	...	...	...	...
...	...	...	...	...

Byte position of the next slot  
 $=76 \times 4 + (\text{Link-P}) \times 8$

FIG.25A



12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
...	...	...	...	...
76	1	—	6	—
77	S	0	N	01
78	Y	/	7	—
79	—	9	j	02
80	G	A	j	—
81	1	0	—	03
82	1	9	j	M
83	I	D	1	04
84	D	—	s	c
85	00	00	00	00
...	...	...	...	...

Byte position of the next slot  
 $=76 \times 4 + (\text{Link-P}) \times 8$

FIG.25B

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
....	....	....	....	....
....	....	....	....	....
76	1	—	7	—
77	S	0	N	01
78	Y	/	/	8
79	—	1	0	02
80	—	G	A	/
81	/	1	1	03
82	—	2	0	—
83	M	—	n	04
84	i	D	—	s
85	c	00	00	00
....	....	....	....	....
....	....	....	....	....

Byte position of the next slot  
 $= 76 \times 4 + (\text{Link-P}) \times 8$

FIG.26A



12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
....	....	....	....	....
....	....	....	....	....
76	1	—	6	—
77	S	0	N	01
78	Y	/	/	7
79	—	9	—	02
80	G	A	/	/
81	1	0	—	03
82	1	9	—	M
83	1	n	—	04
84	D	—	s	c
85	00	00	00	00
....	....	....	....	....

Byte position of the next slot  
 $= 76 \times 4 + (\text{Link-P}) \times 8$

FIG.26B

FIG.27A

		P-TNA 1	P-TNA 2	P-TNA 3
12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
...	...	...	...	...
76	1	—	7	—
77	S	0	N	01
78	Y	/	/	8
79	—	1	0	02
80	;	G	A	/
81	/	1	1	03
82	—	2	0	—
83	M	—	n	04
84	I	D	—	s
85	c	00	00	00
...	...	...	...	...
...	...	...	...	...

Byte position of the next slot  
=76 × 4+(Link-P) × 8



FIG.27B

		P-TNA 1	P-TNA 2	P-TNA 3
12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
...	...	...	...	...
76	1	—	8	—
77	S	0	N	01
78	Y	/	/	9
79	—	1	1	02
80	;	G	A	/
81	/	1	2	03
82	—	2	1	—
83	M	—	n	04
84	I	D	—	s
85	c	00	00	00
...	...	...	...	...
...	...	...	...	...

Byte position of the next slot  
=76 × 4+(Link-P) × 8

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
...	...	...	...	...
76	1	—	7	—
77	S	O	N	01
78	Y	/	/	8
79	—	1	0	02
80	;	G	A	00
81	00	00	00	00
...	...	...	...	...
...	...	...	...	...

Byte position of the next slot  
=76×4+(Link-P)×8

FIG.28A



12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
...	...	...	...	...
76	1	—	7	—
77	S	O	N	01
78	Y	/	/	8
79	—	1	0	02
80	;	G	A	—
81	/	1	1	03
82	—	2	0	—
83	00	00	00	00
...	...	...	...	...
...	...	...	...	...

Byte position of the next slot  
=76×4+(Link-P)×8

FIG.28B

**FIG.29A**

1	2	3	4	5	6	7	8	9	10
12	00000000	P-TNA 1	P-TNA 2	P-TNA 3					
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7					
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11					
...	....	....	....	....					
76	1	—	7	;					
77	S	O	N	01					
78	Y	/	/	8					
79	—	1	0	02					
80	:	G	A	00					
81	00	00	00	00					
...	....	....	....	....					

↓

OVERWRITE PORTION

1	2	3	4	5	6	7	8	9	10	11	12	13

Byte position  
of the next slot  
 $=76 \times 4 + (\text{Link-P}) \times 8$

**FIG.29B**

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3								
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7								
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11								
...	....	....	....	....								
76	1	—	3	;								
77	S	O	N	01								
78	Y	/	/	4								
79	—	8	:	02								
80	/	/	9	—								
81	1	0	:	03								
82	/	/	1	1								
83	—	1	3	04								
84	:	G	A	00								
85	00	00	00	00								
...	....	....	....	....								

Byte position  
of the next slot  
 $=76 \times 4 + (\text{Link-P}) \times 8$